

Amendments to the Drawings:

None

REMARKS/ARGUMENTS

Claims 1-18 stand rejected under 35 U.S.C. 102(e) as being anticipated by Mayur.

In the action dated 3/29/2005, the examiner states, "the examiner finds that a teaching of a temperature above 1026 C., suggests a temperature of at least 1050 C., thus teaching at least one temperature within the range." There is no basis for the examiner to arrive at this conclusion. There is nothing in the cited reference that would lead the examiner to interpret, "a temperature above 1026 C" as suggesting a temperature of at least 1050 C. The above statement means exactly what it describes. It clearly means temperatures above 1026 C which includes 1027 C, 1028 C etc. The examiner's conclusion that the above statement means a temperature of at least 1050 C is clearly erroneous and improper. The claimed temperature range of 1050 – 1350 degrees C is not disclosed in the cited reference.

The cited reference in paragraphs 0077 and 0083 describe the need to use the thermal properties of amorphous silicon in numerical modeling. Representative values of these thermal properties are given in Table II. The heading for table II of, "[T]hermal parameters for amorphous silicon used..." further supports this conclusion. The melting temperature of 1423 K given in Table II is therefore a property of the amorphous silicon used rather than an annealing. The examiner's interpretation of the 1423 K temperature given in Table II is erroneous and improper.

Based on the above, claim 1 is allowable over the Mayur reference under 35 U.S.C. 102(e). Claims 2-4 depend on claim 1 and therefore contain all the limitations of claim 1. Claims 2-4 are therefore also allowable over the cited art.

Examination of all the remaining independent and dependent claims contained in the instant application will show that claims 5-18 all comprise the limitation of annealing said implanted semiconductor with a ultra high temperature anneal comprising

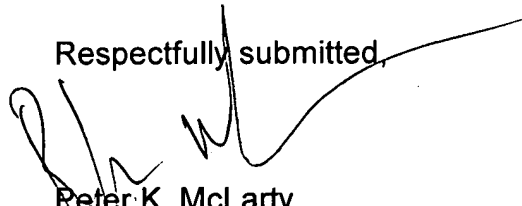
annealing temperatures from 1050°C to 1350°C. Therefore, based on the above, claims 5-18 are therefore also allowable over the cited art.

In light of the above, it is respectfully submitted that the present application is in condition for allowance, and notice to that effect is respectfully requested.

While it is believed that the instant amendment places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, Applicants petition for an Extension of Time under 37 CFR 1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees, to the deposit account of Texas Instruments Incorporated, Account No. 20-0668.

Respectfully submitted,



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